

AMENDMENTS TO THE CLAIMS

1. (Currently amended) A CCD color solid-state image pickup device comprising:
  - a plurality of light-receiving sections arranged in an array on the surface of a semiconductor substrate; and
  - a vertical transfer path by way of which signal electric charges stored in electric charge storage sections of the respective light-receiving sections are read and transferred to a horizontal transfer path,

wherein the electric charge storage section of each of the light-receiving sections has a plurality of electric charge storage layers which are formed in a silicon layer and are provided in a depthwise direction of the semiconductor substrate with potential barriers interposed therebetween; and signal electric charges stored in the respective electric charge storage layers are read independently to the vertical transfer path, and

wherein each of the plurality of electric charge storage layers is different in the depthwise direction from the others of the plurality of electric charge storage layers, and

wherein a first light-receiving section for storing blue (B) and green (G) signal electric charges and a second light-receiving section for storing green (G) and red (R) signal electric charges are alternately provided as the light-receiving sections on the surface of the semiconductor substrate;

the first light-receiving section is provided with a first electric charge storage layer for storing blue (B) signal electric charges and a second electric charge storage layer for storing green (G) signal electric charges; and

the second light-receiving section is provided with the second electric charge storage layer for storing green (G) signal electric charges and a third electric charge storage layer for storing red (R) signal electric charges.

2. (Original) The CCD color solid-state image pickup device according to claim 1, wherein an electric charge path, which causes electric charges stored in the electric charge storage layers to migrate to the surface of the semiconductor substrate and is formed from a heavily-doped impurity region, is provided in an electric charge storage layer from among the

plurality of electric charge storage layers, the electric charge storage layer being provided in the semiconductor substrate.

3. (Previously presented) The CCD color solid-state image pickup device according to claim 1, wherein a concentration gradient is imparted such that the dopant concentration of the electric charge storage layers formed as heavily-doped impurity regions and the dopant concentration of an electric charge path continually connected to an electric charge storage layer increase as the electric charge storage layer and the electric charge path approach the vertical transfer path.

4. (Original) The CCD color solid-state image pickup device according to claim 1, wherein the depths of the respective electric charge storage layers are set in accordance with wavelengths of incident light to be detected.

5. (Canceled)

6. (Canceled)

7. (Canceled)

8. (Currently amended) The CCD color solid-state image pickup device according to claim 7~~claim 1~~, wherein the depth of the first electric charge storage layer ranges from 0.2 to 0.4  $\mu\text{m}$ ; the depth of the second electric charge storage layer ranges from 0.4 to 0.8  $\mu\text{m}$ ; and the depth of the third electric charge storage layer ranges from 0.8 to 2.5  $\mu\text{m}$ .

9. (Original) The CCD color solid-state image pickup device according to claim 1, wherein on-chip light gathering optical systems are provided on upper portions of the respective light-receiving sections, and one opening of each light-shielding film corresponds to one of the light-receiving sections.

10. (Canceled)

11. (Canceled)

12. (Original) The CCD color solid-state image pickup device according to claim 1, wherein the light-receiving sections are arranged in a square grid pattern on the surface of the semiconductor substrate.

13. (Canceled)

14. (Canceled)

15. (Currently Amended) A CCD color solid-state image pickup device comprising:  
a plurality of light-receiving sections arranged in an array on the surface of a  
semiconductor substrate; and

a vertical transfer path by way of which signal electric charges stored in electric charge  
storage sections of the respective light-receiving sections are read and transferred to a horizontal  
transfer path,

wherein the electric charge storage section of each of the light-receiving sections has a  
plurality of electric charge storage layers which are formed in a silicon layer and are provided in  
a depthwise direction of the semiconductor substrate with potential barriers interposed  
therebetween; and signal electric charges stored in the respective electric charge storage layers  
are read independently to the vertical transfer path,

wherein each of the plurality of electric charge storage layers is different in the depthwise  
direction from the others of the plurality of electric charge storage layers,

wherein an electric charge path, which causes electric charges stored in the electric  
charge storage layers to migrate to the surface of the semiconductor substrate and is formed from  
a heavily-doped impurity region, is provided in an electric charge storage layer from among the

plurality of electric charge storage layers, the electric charge storage layer being provided in the semiconductor substrate, and

The CCD color solid-state image pickup device according to claim 2,

wherein a first light-receiving section for storing blue (B) and green (G) signal electric charges and a second light-receiving section for storing green (G) and red (R) signal electric charges are alternately provided as the light-receiving sections on the surface of the semiconductor substrate;

the first light-receiving section is provided with a first electric charge storage layer for storing blue (B) signal electric charges and a second electric charge storage layer for storing green (G) signal electric charges; and

the second light-receiving section is provided with the second electric charge storage layer for storing green (G) signal electric charges and a third electric charge storage layer for storing red (R) signal electric charges.

16. (Canceled)

17. (Canceled)

18. (Currently Amended) A CCD color solid-state image pickup device comprising:  
a plurality of light-receiving sections arranged in an array on the surface of a semiconductor substrate; and

a vertical transfer path by way of which signal electric charges stored in electric charge storage sections of the respective light-receiving sections are read and transferred to a horizontal transfer path,

wherein the electric charge storage section of each of the light-receiving sections has a plurality of electric charge storage layers which are formed in a silicon layer and are provided in a depthwise direction of the semiconductor substrate with potential barriers interposed therebetween; and signal electric charges stored in the respective electric charge storage layers are read independently to the vertical transfer path,

wherein each of the plurality of electric charge storage layers is different in the depthwise direction from the others of the plurality of electric charge storage layers,

wherein a concentration gradient is imparted such that the dopant concentration of the electric charge storage layers formed as heavily-doped impurity regions and the dopant concentration of an electric charge path continually connected to an electric charge storage layer increase as the electric charge storage layer and the electric charge path approach the vertical transfer path, and

The CCD color solid-state image pickup device according to claim 3,

wherein a first light-receiving section for storing blue (B) and green (G) signal electric charges and a second light-receiving section for storing green (G) and red (R) signal electric charges are alternately provided as the light-receiving sections on the surface of the semiconductor substrate;

the first light-receiving section is provided with a first electric charge storage layer for storing blue (B) signal electric charges and a second electric charge storage layer for storing green (G) signal electric charges; and

the second light-receiving section is provided with the second electric charge storage layer for storing green (G) signal electric charges and a third electric charge storage layer for storing red (R) signal electric charges.

19. (Canceled)